QUICKSWITCH® PRODUCTS HIGH-SPEED CMOS 20-BIT BUS SWITCH

IDTQS32X2384

FEATURES:

- Enhanced N channel FET with no inherent diode to Vcc
- Low propagation delay and zero ground bounce
- 25Ω resistors for low noise
- Undershoot Clamp Diodes on all switch and control Inputs
- Four enables control five bits each
- TTL-compatible input and output levels
- Available in 48-pin QVSOP package

APPLICATIONS:

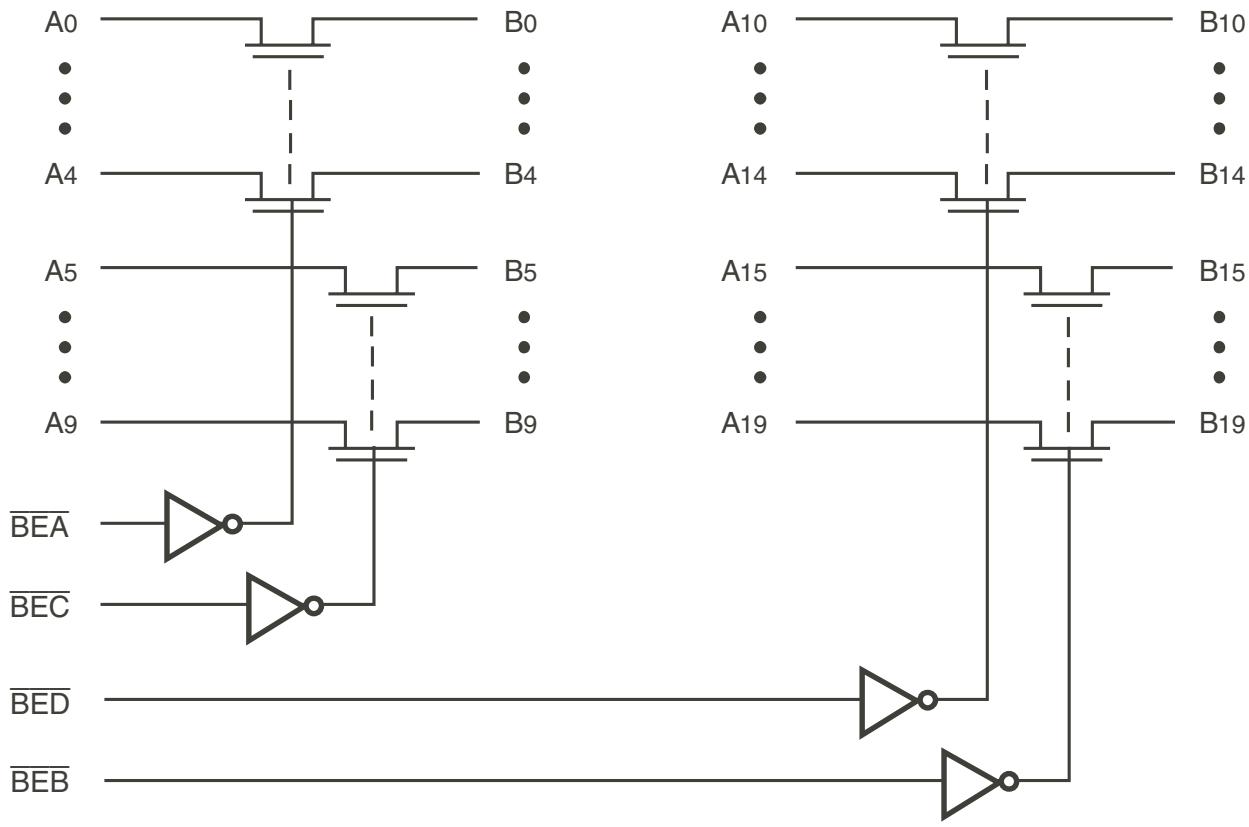
- Hot-swapping, hot-docking
- Voltage translation (5V to 3.3V)
- Power conservation
- Capacitance reduction and isolation
- Bus isolation
- Clock gating

DESCRIPTION:

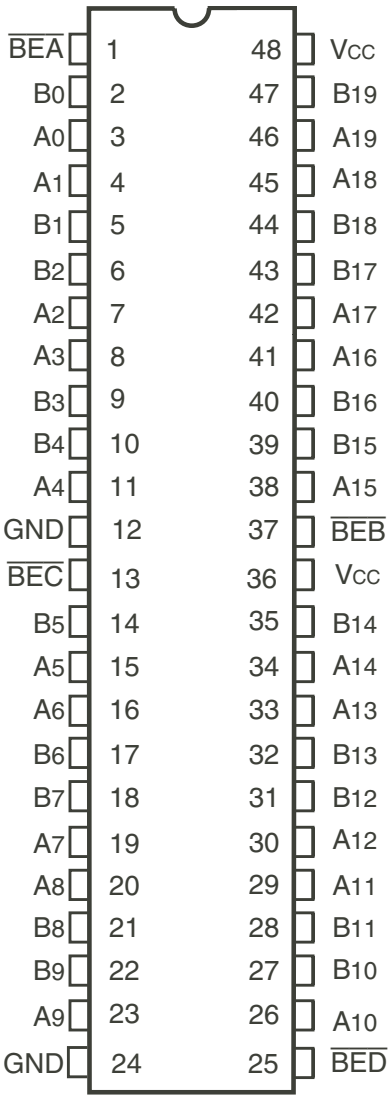
The QS32X2384 provides a set of twenty high-speed CMOS TTL-compatible bus switches. The QS32x2384 also includes internal 25Ω series termination resistors to reduce reflection noise in high-speed applications. The Bus Enable ($\overline{BE}$) signals turn the switches on. Four Bus Enable signals are provided, one for each of five bits of the 20-bit bus. The '384 family of QuickSwitch products is ideal for switching wide digital buses, as well as hotplug buffering, and 5V to 3V conversion.

The QS32X2384 is characterized for operation at -40°C to +85°C.

FUNCTIONAL BLOCK DIAGRAM



PIN CONFIGURATION



QVSOP
TOP VIEW

PIN DESCRIPTION

Pin Names	I/O	Description
A_0 - A_{19}	I/O	Bus A
B_0 - B_{19}	I/O	Bus B
$\overline{BEA}$	I	Enable, 0 - 4
$\overline{BEB}$	I	Enable, 15 - 19
$\overline{BEC}$	I	Enable, 5 - 9
$\overline{BED}$	I	Enable, 10 - 14

ABSOLUTE MAXIMUM RATINGS(1)

Symbol	Description	Max	Unit
V_{TERM}(2)	SupplyVoltage to Ground	–0.5 to +7	V
V_{TERM}(3)	DC Switch Voltage V_S	–0.5 to +7	V
V_{TERM}(3)	DC Input Voltage V_{IN}	–0.5 to +7	V
V_{AC}	AC Input Voltage (pulse width ≤20ns)	–3	V
I_{OUT}	DC Output Current (max. sink current/pin)	120	mA
P_{MAX}	Maximum Power Dissipation (T_A = 85°C)	0.5	W
T_{STG}	Storage Temperature	–65 to +150	°C

NOTES:
1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
2. V_{CC} terminals.
3. All terminals except V_{CC} .

CAPACITANCE (T_A = +25°C, F = 1MHz, V_{IN} = 0V, V_{OUT} = 0V)

Pins	Typ.	Max.(1)	Unit
Control Inputs	3	5	pF
Quickswitch Channels (Switch OFF)	5	7	pF

NOTE:
1. This parameter is guaranteed but not production tested.

FUNCTION TABLE(1)

$\overline{BEA}$	$\overline{BEB}$	B_0 - B_4	B_{15} - B_{19}	Function
H	H	Z	Z	Disconnect
L	H	A_0 - A_4	Z	Connect
H	L	Z	A_{15} - A_{19}	Connect
L	L	A_0 - A_4	A_{15} - A_{19}	Connect
$\overline{BEC}$	$\overline{BED}$	B_5 - B_9	B_{10} - B_{14}	Function
H	H	Z	Z	Disconnect
L	H	A_5 - A_9	Z	Connect
H	L	Z	A_{10} - A_{14}	Connect
L	L	A_5 - A_9	A_{10} - A_{14}	Connect

NOTE:
1. H = HIGH Voltage Level
   L = LOW Voltage Level
   X = Don't care
   Z = High-Impedence

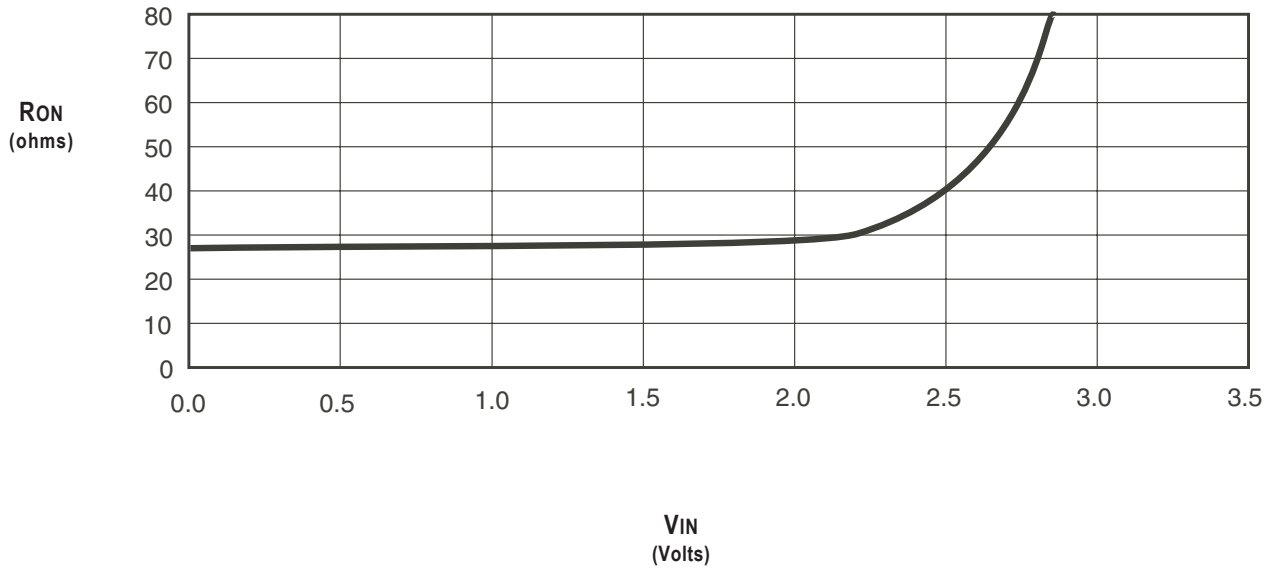
DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:
Industrial: T_A = –40°C to +85°C, V_{CC} = 5.0V ± 5%

Symbol	Parameter	Test Conditions	Min.	Typ.[1]	Max.	Unit
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH for Control Pins	2	—	—	V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW for Control Pins	—	—	0.8	V
I_{IN}	Input Leakage Current (Control Inputs)	0V ≤ V_{IN} ≤ V_{CC}	—	±0.01	±1	µA
I_{OZ}	Off-State Current (Hi-Z)	0V ≤ V_{OUT} ≤ V_{CC}, Switches OFF	—	±0.01	±1	µA
R_{ON}	Switch ON Resistance[2]	V_{CC} = Min., V_{IN} = 0V, I_{ON} = 30mA	20	28	40	Ω
		V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15mA	20	35	48	
V_P	Pass Voltage[3]	V_{CC} = 5V, I_{OUT} = -5µA	3.7	4	4.2	V

NOTES:
1. Typical values are at V_{CC} = 5.0V and T_A = 25°C.
2. Max value of R_{ON} is guaranteed but not production tested.
3. Pass voltage is guaranteed but not production tested.

TYPICAL ON RESISTANCE vs V_{IN} AT V_{CC} = 5V



POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions[1]	Max.	Unit
I_{CCQ}	Quiescent Power Supply Current	V_{CC} = Max., V_{IN} = GND or V_{CC}, f = 0	3	mA
ΔI_{CC}	Power Supply Current per Input HIGH[2]	V_{CC} = Max., V_{IN} = 3.4V, f = 0	2.5	mA
I_{CCD}	Dynamic Power Supply Current per MHz[3]	V_{CC} = Max., A and B Pins Open Control Input Toggling @ 50% Duty Cycle	0.25	mA/MHz

NOTES:
1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
2. Per TTL driven input (V_{IN} = 3.4V, control inputs only). A and B pins do not contribute to ΔI_{CC}.
3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

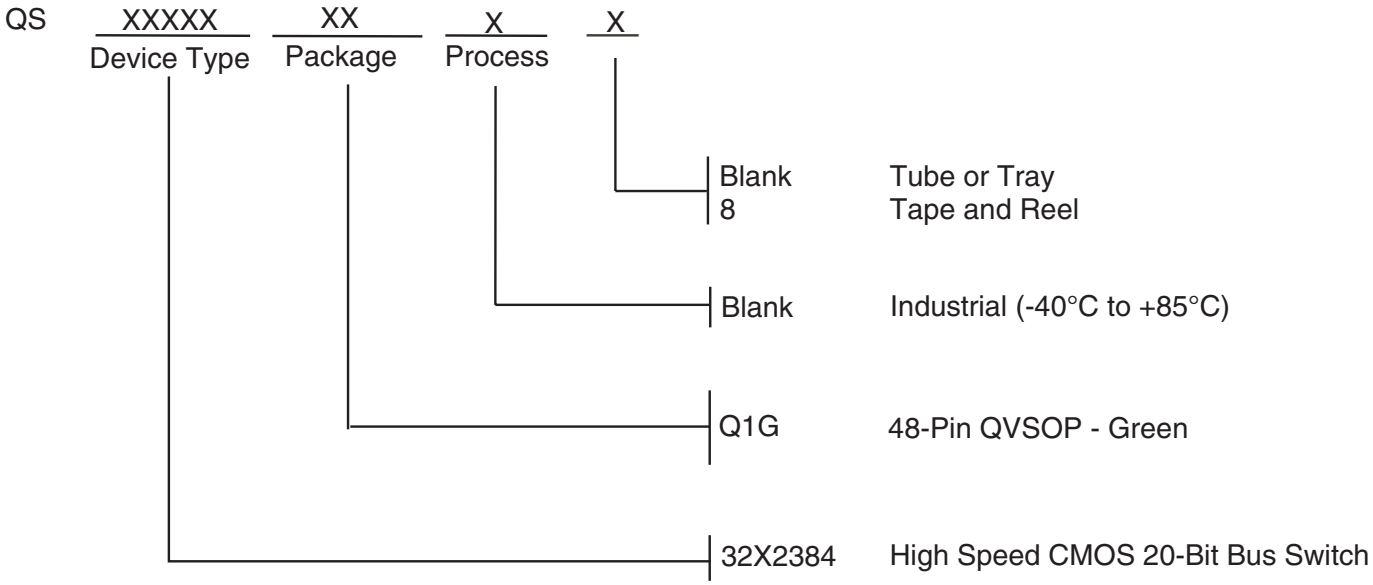
SWITCHING CHARACTERISTICS OVER OPERATING RANGE

T_A = -40°C to +85°C

Symbol	Parameter	Min.[1]	Typ.	Max.	Unit
t_{PLH} t_{PHL}	Data Propagation Delay[2,4] Ax to Bx, Bx to Ax	—	—	1.25[3]	ns
t_{PZL} t_{PZH}	Switch Turn-On Delay $\overline{BE}x$ to Ax, Bx	1.5	—	7.5	ns
t_{PLZ} t_{PHZ}	Switch Turn-Off Delay[2] $\overline{BE}x$ to Ax, Bx	1.5	—	5.5	ns

NOTES:
1. Minimums are guaranteed but not production tested.
2. This parameter is guaranteed but not production tested.
3. The time constant for the switch alone is of the order of 1.25ns at C_L = 50pF.
4. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

ORDERING INFORMATION



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Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan

www.renesas.com

Contact Information

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